

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Mostafazadeh et al.

Attorney Docket No.:

NSC1P236C1/P05221

Application No.: 10/714,028

Examiner: Zarneke, David A.

Filed: November 14, 2003

Group: 2891

Title: BUMPED INTEGRATED CIRCUITS FOR
OPTICAL APPLICATIONS**CERTIFICATE OF TRANSMISSION**

I hereby certify that this correspondence is being transmitted via facsimile to the U.S. Patent and Trademark Office, Attention: Examiner Zarneke, David A. at facsimile telephone number (571) 273-8300 on November 7, 2005.

Printed Name: Natalie MorganSigned: **TERMINAL DISCLAIMER UNDER 37 C.F.R. § 1.321**
TO OBLATE AN OBVIOUSNESS-TYPE
DOUBLE PATENTING REJECTION

Commissioner for Patents
P.O. Box 1450
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Sir:

National Semiconductor Corporation owns the entire interest in the above-identified application and in the prior United States Patent No. 6,707,148 (hereafter "prior patent"). National Semiconductor Corporation hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. §§154 to 156 and §173 of the prior patent. National Semiconductor Corporation hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the patent and the prior patent are commonly owned. This agreement runs with any patent granted on the above-identified application and is binding upon the grantee, its successors or assigns.

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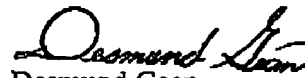
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The undersigned is an agent of record.

If the required fees (E.g., the amount of \$110 to cover the disclaimer fee pursuant to 37 C.F.R. §1.20(d)) are not enclosed or additional fees are required to facilitate filing of this paper, please charge such fees or credit any overpayment to Deposit Account No. 500388 (Order No. NSC1P236C1).

Respectfully submitted,

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